



Patent Application
Attorney Docket No.: 57941.000013
Client Reference No.: RA264

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: :
:
Jared LeVan ZERBE et al. :
: Group Art Unit: Unassigned
Appln. No.: 10/673,677 :
: Examiner: Unassigned
Filed: September 30, 2003 :
:
For: TECHNIQUE FOR RECEIVING :
DIFFERENTIAL MULTI-PAM :
SIGNALS :

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicant(s) hereby submits attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure.

The cited references, while believed to be of some relevance, are not necessarily considered to teach or suggest any aspect of the invention described and claimed in the above-identified patent application. Applicant(s) hereby expressly reserves the right to swear behind the effective dates of any of the cited references. Applicant(s) further reserves the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this information disclosure statement. This information disclosure statement is also not to be construed as a representation that a search has, or has not, been conducted or that no better art exists. Rather, this information disclosure statement discloses only the best references of which the associated individuals are aware.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicant(s).

For the convenience of the Examiner in considering the cited references, a copy of each of the cited references is enclosed with this communication. In considering the cited

references, it may be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of the cited references. Any such markings were either present on the copies of the cited references obtained by the associated individuals, or were made thereon during the study of the references by the associated individuals.

In accordance with 37 CFR § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 CFR § 1.491; or (iii) before the mailing date of a first Office Action on the merit for the above-identified patent application. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

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Respectfully submitted,

HUNTON & WILLIAMS

By: 

Thomas E. Anderson

Registration No. 37,063

TEA/sdw
Hunton & Williams
1900 K Street
Washington, DC 20006
Telephone: (202) 955-1808
Facsimile: (202) 778-2201
Date: December 29, 2003

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INVENTOR'S NAME:
Jared LeVan ZERBE et al.

EXAMINER:
Unassigned

FILING DATE:
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GROUP:
Unassigned

LIST OF MATERIALS CITED BY APPLICANT

(Use several sheets if necessary)

Page 1 of 1

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS AND SUBCLASS	TRANSLATION PROVIDED	
				Yes	No

OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)

1.	M. RAU et al., "Clock/Data Recovery PLL Using Half-Frequency Clock," IEEE Journal Of Solid-State Circuits, Vol. 32, No. 7, July 1997, pp. 1156-1159
2.	CHIH-KONG et al., "A 0.8- μ m CMOS 2.5 Gb/s Oversampling Receiver and Transmitter for Serial Link," IEEE Journal Of Solid-State Circuits, Vol. 31, No. 12 December 1996, pp. 2015-2023
3.	RAMIN FARJAD-RAD et al., "A 0.3- μ m CMOS 8-Gb/s 4-PAM Serial Link Transceiver," IEEE Journal Of Solid-State Circuits, Vol. 35, No. 5, May 2000, pp. 757-764
4.	KUN-YUNG KEN CHANG et al., "A 0.4-4Gb/s CMOS Quad Transceiver Cell using On-chip Regulated Dual-Loop PLLs," 4 pages

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.